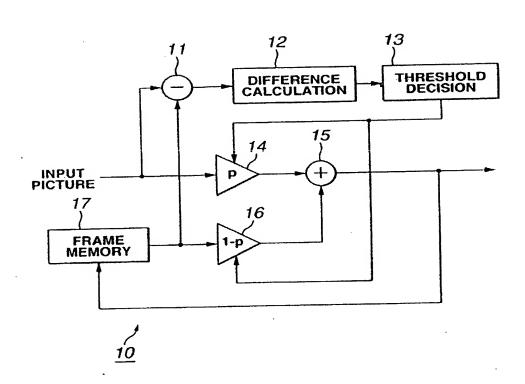


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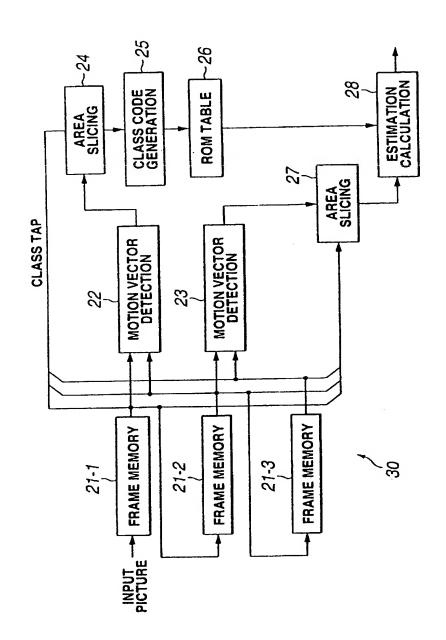
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**FIG.1** (PRIOR ART)

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(PRIOR ART) **FIG.2** 

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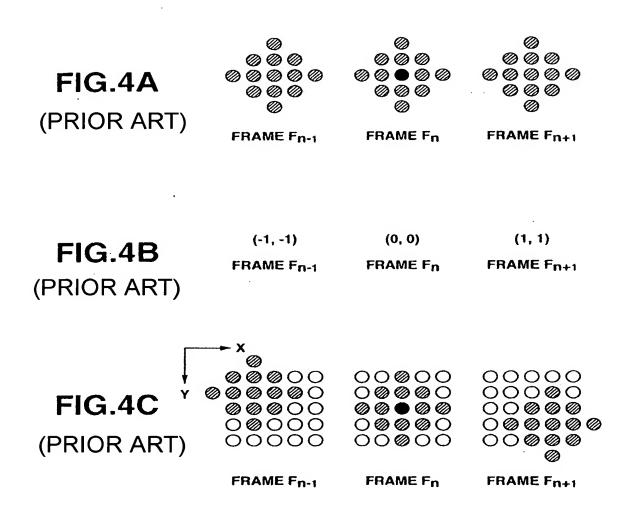
FIG.3A	Ø	●	Ø
(PRIOR ART)	FRAME F <sub>n-1</sub>	FRAME Fn	FRAME F <sub>n+1</sub>
FIG.3B	(-1, -1)	(0, 0)	(1, 1)
(PRIOR ART)	FRAME F <sub>n-1</sub>	FRAME F <sub>n</sub>	FRAME F <sub>n+1</sub>
FIG.3C (PRIOR ART)	→ X ○○○○ ○◎○○ ○○○○ ○○○○ FRAME F <sub>n-1</sub>	0000 0000 0000 0000 0000	00000 00000 00000 00000 FRAME Fn+1

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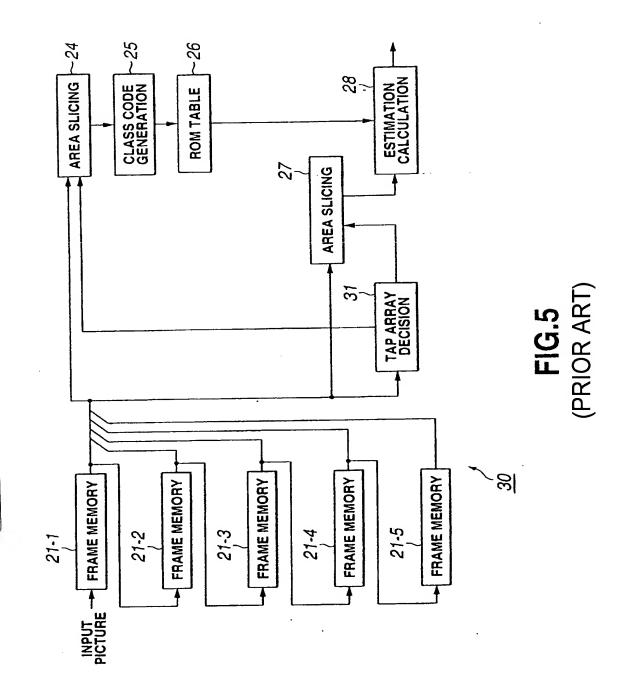


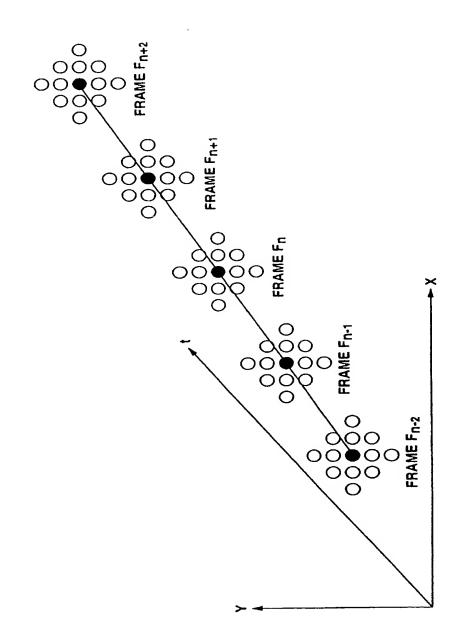
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**FIG.6** (PRIOR ART)

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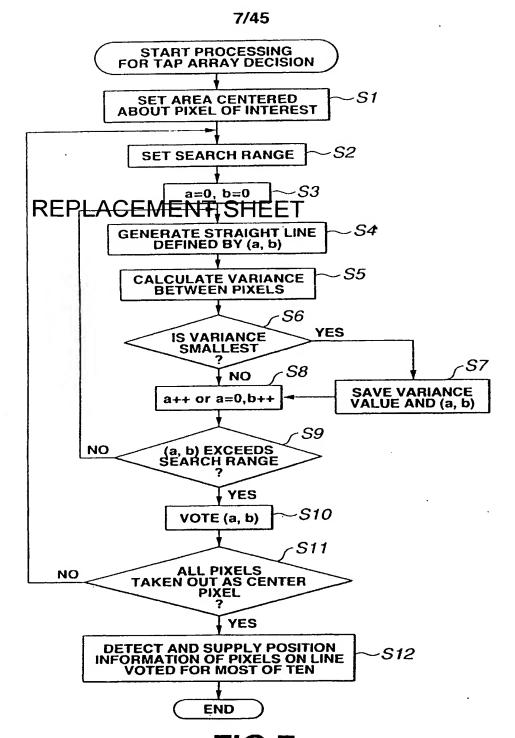
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**FIG.7** (PRIOR ART)

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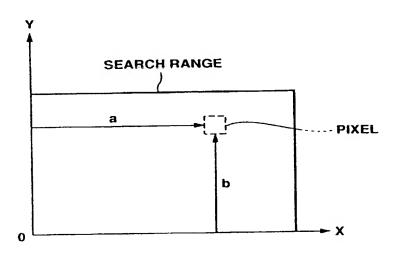


FIG.8

(PRIOR ART)

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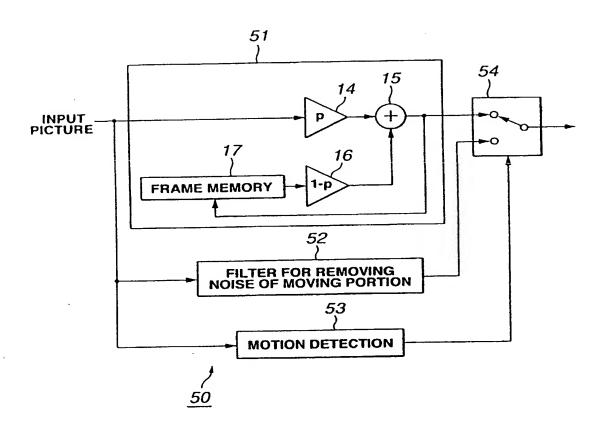


FIG.9 (PRIOR ART)

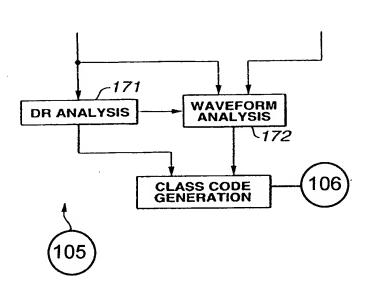
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**FIG.19** 

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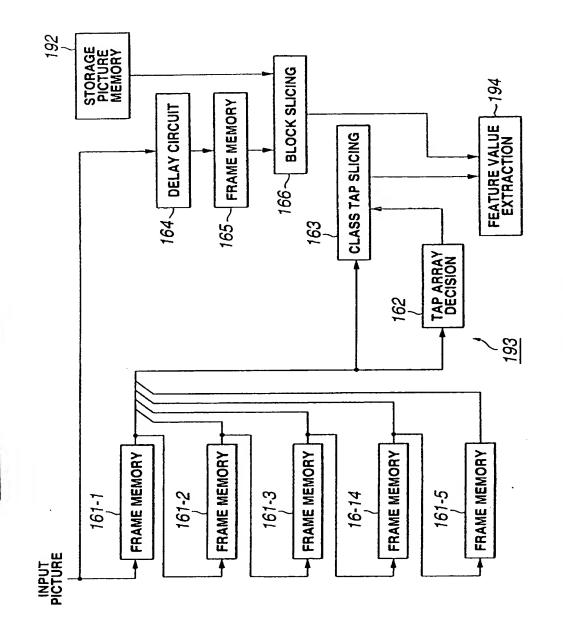


Image Processing Device and Method and Record-

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FIG.22